

ABSTRACT

The present invention relates to a data processing apparatus, a method and apparatus for encoding, a method and apparatus for decoding, and a program, that allow a reduction in an algorithm delay. An interpolator 51 produces interpolated PCM data by performing R-times oversampling on original PCM data. A frame encoder 54 fetches a predetermined number of samples of the oversampled data as one frame, encodes the oversampled data on a frame-by-frame basis, and outputs resultant encoded data. A frame decoder 55 decodes the encoded data on a frame-by-frame basis at a rate R times higher than a predetermined normal rate. A decimator 56 decimates data obtained as a result of the decoding such that the number of samples is reduced to $1/R$ of the number of samples included in the original data. The present invention is applicable, for example, to an IP telephone system.